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DATE: Tuesday, November 09, 2004

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		<i>DB=USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L9	(selective\$4 near5 (stor\$4 or buffer\$4)) same ((determin\$4 or calculat\$4 or assess\$4 or verif\$9) near3 power near3 (condition or state))	1
<input type="checkbox"/>	L8	((read\$4 or writ\$4 or access\$4) near5 (portion or partial\$4 or section or segment)) same ((determin\$4 or calculat\$4 or assess\$4 or verif\$9) near3 power near3 (condition or state))	6
<input type="checkbox"/>	L7	L4 and ((determin\$4 or calculat\$4 or assess\$4 or verif\$9) near3 power near3 (condition or state))	62
<input type="checkbox"/>	L6	L4 same ((determin\$4 or calculat\$4 or assess\$4 or verif\$9) near3 power near3 (condition or state))	2
<input type="checkbox"/>	L5	L4 with ((determin\$4 or calculat\$4 or assess\$4 or verif\$9) near3 power near3 (condition or state))	0
<input type="checkbox"/>	L4	(receiv\$4 near3 (request or instruction or command) near3 (writ\$4 or read\$4 or (memory near2 access\$4)))	14183
<input type="checkbox"/>	L3	L1 with ((determin\$4 or calculat\$4 or assess\$4 or verif\$9) near3 power near3 (condition or state))	12
<input type="checkbox"/>	L2	L1 same ((determin\$4 or calculat\$4 or assess\$4 or verif\$9) near3 power near3 (condition or state))	33
<input type="checkbox"/>	L1	(receiv\$4 near3 (request or instruction or command))	181705

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L6: Entry 1 of 2

File: USPT

Sep 23, 2003

DOCUMENT-IDENTIFIER: US 6624962 B1

**** See image for Certificate of Correction ****

TITLE: Storage device and method in which write/read fault errors are not generated when mode is switched from power save mode or migration mode to ordinary mode

Detailed Description Text (7):

A current mode determining section 200b determines whether the current mode is power save mode or migration mode when the read/write command is received by the command receiving section 200a. A command executing section 200c executes a command corresponding to a determination result of the current mode determining section 200b. Command to be executed by the command executing section 200c will be described later. A mode condition determining section 200d determines a condition for the power save mode and a condition for the migration mode.

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L3: Entry 3 of 12

File: USPT

Mar 19, 2002

DOCUMENT-IDENTIFIER: US 6360327 B1

TITLE: System with control registers for managing computer legacy peripheral devices using an advanced configuration power interface software power management system

CLAIMS:

46. A computer system, comprising:

a means for executing operating system instructions;

a means, coupled to said means for executing, for displaying video images;

a peripheral device, coupled to said means for executing, for providing a communications interface;

a means for determining the power state of said computer system, said means for determining coupled to said means for executing and including a means for storing said power state, said means for storing configured to receive power transition requests generated by said operating system instructions, wherein said means for determining controls the power state of said computer system based on the contents of said means for storing;

a register for receiving power transition requests, said register coupled to said means for executing, and wherein the highest byte of said register includes an enable bit;

a means for transmitting an interrupt to said means for executing when said enable bit is set; and

wherein said means for executing, in response to receiving said interrupt, is capable of configuring said peripheral device for low-power mode and of transferring the contents of said register to said means for storing.

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